

AMENDMENTS TO THE CLAIMS

Please **AMEND** claims 1-6 and 8 as shown below.

Please **ADD** claims 9-12 as shown below.

This listing of claims will replace all prior versions, and listings, of claims in the application.

1. (Currently Amended) A method for driving a plasma display panel including a scan electrode and a sustain electrode provided in parallel on a first substrate, and an address electrode provided on a second substrate, the address electrode crossing the scan electrode and the sustain electrode, the method comprising:

during a reset period,

applying a rising ramp voltage to the sustain electrode up to a first voltage level, and ~~erasing wall charges,~~ after a previous sustain period is complete;

maintaining the address electrode and the sustain electrode at a second voltage ~~when the erasing is finished,~~ and applying a rising ramp voltage to the scan electrode, the rising ramp voltage gradually rising from a third voltage to a fourth voltage, the third voltage being less than below the a discharge firing voltage with respect to the sustain electrode and the fourth voltage being greater than the to a third voltage over a discharge firing voltage;

applying ~~when the step of maintaining is finished, wherein~~ a falling ramp voltage to the scan electrode while maintaining the sustain electrode at ~~[[the]]~~ a first bias voltage, the falling ramp voltage gradually ~~falls~~ falling to a predetermined voltage from the ~~fourth~~ third voltage; and

maintaining the sustain electrode at a second bias voltage below the first bias voltage while maintaining the scan electrode at the predetermined voltage after the applying a falling ramp voltage of the sustain electrode during the predetermined voltage maintain period of the scan electrode formed after finishing the applying step.

2. (Currently Amended) The method of claim 1, wherein the second voltage is ~~[[OV]]~~ a reference voltage.
3. (Currently Amended) The method of claim 1, wherein the ~~level of the~~ second bias voltage ~~of the sustain electrode~~ is substantially identical to the ~~fourth~~ third voltage ~~level~~.
4. (Currently Amended) The method of claim 1, wherein the second voltage is a negative voltage ~~sustain electrode is maintained below 0 volts during the step of maintaining the address electrode~~.
5. (Currently Amended) The method of claim 1, wherein ~~the scan electrode ramp falls from the fourth voltage to the predetermined voltage and maintained the predetermined voltage~~ has an equal magnitude and opposite polarity to the third voltage.
6. (Currently Amended) A plasma display panel driver, comprising:
a plasma panel ~~for providing~~ comprising a plurality of address electrodes, and first ~~electrode electrodes~~ and a second ~~electrode electrodes~~ crossing the address electrodes, the first electrodes and the second electrodes being in pairs and parallel to each other, and a crossing area of the address electrode and the first electrodes and the second electrodes forming a discharge cell;
a controller for externally receiving video signals, and generating an address driving signal, first electrode driving signals and second electrode driving signals;

an address driver for receiving the address driving signal from the controller, and applying a display data signal for selecting a discharge cell to be displayed to the address electrode;

a first driver for receiving the first electrode driving signals from the controller, and applying a voltage to a first electrode of a discharge cell selected for discharge so as to generate discharge ~~[[to]]~~ in the discharge cell ~~first electrode~~; and

a second driver for receiving the second electrode driving signals from the controller, and applying a voltage to ~~[[the]]~~ a second electrode of the discharge cell selected for discharge so that the discharge cell selected for discharge may maintain discharging for a predetermined time,

wherein the first driver applies a voltage that is ramp-risen to a first voltage level to the first electrode, maintains the voltage at a second voltage level below the first voltage level, ramp-falls the voltage to a third voltage level, and maintains the ~~ramp-fallen~~ voltage at the third voltage level, and

wherein the second driver applies a first bias voltage to the second electrode while the voltage at the first electrode is ramp-fallen to the third voltage level ~~during the ramp-falling period of the first electrode~~, and applies a second bias voltage below the first bias voltage to the second electrode while the first electrode is maintained at ~~[[a]]~~ the third voltage level.

7. (Original) The PDP driver of claim 6, wherein the voltage level of the second bias voltage is substantially identical to the second voltage level of the first electrode.

8. (Currently Amended) A method for driving a plasma display including a scan electrode and a sustain electrode provided in parallel on a first substrate, and an address

electrode provided on a second substrate, the address electrode crossing the scan electrode and the sustain electrode, the method comprising:

during a reset period,

applying a falling ramp voltage to the scan electrode and applying a first bias voltage to the sustain electrode; and

applying a second bias voltage to the sustain electrode, the second bias voltage having a voltage level lower than a voltage level of the first bias voltage, and applying a predetermined voltage to the scan electrode after applying the falling ramp voltage,

wherein the falling ramp voltage falls to the predetermined voltage.

~~after applying a falling ramp voltage to the scan electrode such that the scan electrode reaches a predetermined voltage, reducing a voltage of the sustain electrode such that a voltage difference between the scan electrode and the sustain electrode is reduced.~~

9. (New) The method of claim 1, wherein the first bias voltage is substantially identical to the first voltage.

10. (New) The method of claim 1, wherein the predetermined voltage is a reference voltage.

11. (New) The method of claim 1, wherein the predetermined voltage is a negative sustain voltage.

12. (New) A method for driving a plasma display panel including a scan electrode and a sustain electrode provided in parallel on a first substrate, and an address electrode provided on

a second substrate, the address electrode crossing the scan electrode and the sustain electrode, the method comprising:

during a reset period,

applying a ramp voltage to the sustain electrode to a first voltage after a previous sustain period is complete;

maintaining the address electrode at a second voltage, maintaining the sustain electrode at a third voltage, and applying a rising ramp voltage to the scan electrode, the rising ramp voltage gradually rising from a fourth voltage to a fifth voltage, the fourth voltage being less than a discharge firing voltage with respect to the sustain electrode and the fifth voltage being greater than the discharge firing voltage;

applying a falling ramp voltage to the scan electrode while maintaining the sustain electrode at a first bias voltage, the falling ramp voltage gradually falling to a predetermined voltage from the fourth voltage; and

maintaining the sustain electrode at a second bias voltage below the first bias voltage while maintaining the scan electrode at the predetermined voltage after the applying a falling ramp voltage.